

ABSTRACT

In the processing device (10) in accordance with the present invention, a plurality of divided circuits obtained by dividing one circuit are successively configured 5 on a reconfigurable circuit (12), an operation is executed by the divided circuits by feeding back an output of one divided circuit to a next divided circuit, and an output is taken out from the last configured divided circuit. As a feedback path, a path portion (24) is formed, which connects the output of the reconfigurable circuit (12) to its input. By successively configuring the divided circuits, one circuit as a whole can be 10 implemented.